IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Takashi OKUDA et al.) Group Art Unit: Unassigned
Application No.: Unassigned) Examiner: Unassigned
Filed: November 19, 2003) Confirmation No.: Unassigned
For: MODULATOR PROVIDING ONLY QUANTIZATION ERROR)
COMPONENT TO $\Delta\Sigma$)
MODULATOR)

FIRST INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the cited documents is enclosed.

Japanese Patent Nos. 3-22626, 6-53836, 11-308110, and 2002-76902 are discussed in the specification.

The documents are being submitted within three (3) months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later. Since the documents are being filed within the time period set forth in 37 C.F.R. § 1.97(b) no fee or statement is required.

Information Disclosure Statement Application No. <u>Unassigned</u> Attorney's Docket No. <u>009683-486</u> Page 2

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner-initialed copy of this form be returned to the undersigned.

By:

Respectfully submitted,

BURNS, DOAME, SWECKER & MATHIS, L.L.P.

Date: November 19, 2003

Platon N. Mandros Registration No. 22,124

P.O. Box 1404 Alexandria, Virginia 22313-1404 (703) 836-6620 Substitute for forms 1449A/PTO & 1449B/PTO

FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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ATTORNEY'S DKT No. 009683-486	APPLICATION No. Unassigned
APPLICANT Takashi OKUDA et al.	
FILING DATE November 19, 2003	GROUP Unassigned

		U.S	S. PATENT DOCUMENTS			
Examiner Initials	Document Kind Code Name of Patentee or Applicant Number (if known) of Cited Document			Issue/Publication Date (MM-DD-YYYY)		
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		FORE	IGN PATENT DOCUMENTS			Š.,
Examiner Initials	Document Number	Kind Code (if known)	Country	Date of Publication (MM-DD-YYYY)		
	*3-22626	(Japan	01-31-1991	Abst	Ϊ́
	*6-53836		Japan	02-25-1994	Abst	
	*11-308110		Japan	11-05-1999	Abst	
	*2002-76902			03-15-2002	Abst	
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	wa.	NON PATE	ENT LITERATURE DOCUMENT	S		10 T
Examiner Initials	Include item (book	, magazine, journal,	CAPITAL LETTERS), title of the article serial, symposium, catalog, etc.), date, ublisher, city and/or country where publi-	(when appropriate), title page(s), volume-issue n	of the	<u>- e 5</u>
	Gabor C. TEMES et a	I., "Architectures fo	r ΔΣ DACs," Delta-Sigma Data Converte	ers, IEEE Press, 1997, pr	o. 309-313.	
	David JOHNS et al., "	Analog Integrated C	Circuit Design," John Wiley & Sons Inc.,	1997, pp. 547-551, 568-5	571.	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

^{*}Also discussed in the specificati n